

Veljko Milutinovic's CV

Full Name: Veljko Milutinovic

Date of Birth or Age: May 4, 1951.

Title and Primary Current Affiliation(s):

The CEO of IPSI Belgrade, d.o.o.
(since march 2003, a spin-off of the German Fraunhofer IPSI Research Institute).

Visiting Professor, University of Beograd, Serbia (elected in 2018),
Visiting Professor, University of Kragujevac, Serbia (elected in 2021),
Visiting Professor, Technical University of Vienna, Austria (elected in 2018),
Adjunct Professor, Technical University of Graz, Austria (elected in 2023),
Adjunct Professor, Indiana University, Bloomington, Indiana (elected in 2016),
Guest Lecturer, Purdue University, West Lafayette, Indiana (since 2017).

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Interests, Activities, Awards, and Services

Professor Veljko Milutinovic is a Life Fellow of the IEEE,
a Life Member and a Former Honorary Treasurer of Academia Europaea (AE),
formed with support of the British Royal Society,
the German National Academy Leopoldina,
and the Swedish Rijks Akademien.

He is a founding member of the Serbian National Academy of Engineering,
and a Foreign Member of the Montenegrin National Academy of Sciences and Arts.

His single-author book on GaAs microprocessor design, elaborated in [4],
was the Best Seller of All Times for IEEE Computer Society Press in 1986,
and his co-authored paper about the 4096-processor Systolic Array for DARPA
won a Honorary Mention at the IEEE/ACM HICSS-1986, elaborated in [3].

On the topic of DataFlow SuperComputing
he lectured at almost all major universities in the USA
(MIT, Harvard, Purdue, Indiana, CMU, GATECH, Columbia, NYU, Illinois, Michigan, etc...),
and at over 100 universities worldwide.

Related invited talks he delivered at dozens of conferences worldwide,
often as a Keynote, or as the Opening Keynote (e.g., EMiT-2019 in England, UK).

For 20 of his books or special issues of journals,
forewords or endorsements were written by 20 different Nobel Laureates
with whom he cooperated on his past industry sponsored projects,
while his most recent cooperation has been oriented to the research of
Jerome Friedman (Quark on DataFlow) and Dan Schechtman (QuasiCrystals),
and dataflow-based implementations of simulators for processes they discovered.

He has over 100 SCI journal papers (mostly in IEEE and ACM journals),
over 40 books with the USA publishers
(4 as a single author, 16 co-authored, and over 20 edited or co-edited),
over 2000 WoS citations,
over 2000 SCOPUS citations,
and close to 6000 Google Scholar citations ($h=40$).

He consulted for industry, like AeroSpace Corporation, ElectroSpace Corporation,
RCA AeroSpace and Defense, Boeing/Panthesis,
HP Encore Labs in Florida, AT&T Bell Labs in New Jersey,
NCR, StorageTek, Dow Jones, Wall Street Journal, and many others.

He was invited, through in-house presentations,
to share his architecture/design solutions also
at the World Bank (WB) and for experts of Int'l Monetary Fund (IMF), both in Washington DC,
Brookhaven National Laboratory, Lawrence Livermore National Laboratory,
IBM TJ Watson, Honeywell, Intel Oregon, Qualcomm VP, Fairchild, Tektronix,
Yahoo NY, Google CA, Microsoft, Finsoft, NASA Cape Canaveral, JPL Pasadena,
CERN Research Seminar, CERN Computing Center,
ABB Zurich, Oracle Zurich, HLRS in Stuttgart, IPSI Fraunhofer in Darmstadt,
and many other industrial labs or physics labs.

His lecture on future graph-based DataFlow computing for real-time simulation
of processes in physics was presented for researchers of all major physics labs of the USA.

He served as the Senior Advisor at Maxeler Technologies in Sunnyvale, California, USA, and for TechnologyConnect, Boston, Massachusetts, USA.

He enjoys teaching his DataFlowAcceleration and SoftwareEngineeringManagement courses, since 2000.

Optional Addendum:

Veljko Milutinovic was born in Belgrade, Serbia, Yugoslavia. His B.Sc., M.Sc., and Ph.D. degrees are from the School of Electrical Engineering at the University of Belgrade, obtained in parallel with his full-time employment in the nation's leading defense industry research institutes (Michael Pupin and Nikola Tesla), where he worked as the principal designer who successfully completed (till serial production) several highly sophisticated engines for DSP of data buried in additive (Gaussian) and multiplicative (Fading) noises. At that time he also completed (till serial production) several engines for execution-time-efficient mapping of HLL programs into firmware, to obtain maximal acceleration. After Ph.D. he joined Purdue University as a visiting assistant professor for 6 years, but he left soon after he was promoted into a tenure-track position, which was six years before his tenure-decision deadline, in order: (a) to get himself involved full-time as a consultant and sub-contractor for several attractive cutting-edge defense-oriented and industrial-research projects for leading high-tech companies of the USA (NCR, AT+T Bell Labs, Encore, StorageTek, Wall Street Journal, Dow Jones, Maxeler, MindGenomics, etc...), and (b) to get himself involved in the industry-oriented and the academy-oriented dissemination of the obtained research results (mostly for Maxeler, a Stanford spin-off, and MindGenomics, a Harvard spin-off). However, he continued to be on the Purdue University payroll for six more years (till mid 1995) and was spending his next six summers at Purdue (till mid 2001), in order to deliver on his research grants that were sponsored through Purdue, and were awarded to Purdue prior to him leaving his tenure-track position at Purdue. For the last six years (since 2017), each semester, he serves as a Guest Lecturer at Purdue University ECE and CEE departments.

His research in GaAs MicroProcessors and DataFlow SuperComputers was highly inspired and influenced by four different Nobel Laureates: (a) by Nobel Laureate Richard Feynman (under Feynman's influence, Milutinovic insisted on dataflow graphs with shortest possible edges, for minimal waste of energy), (b) by Nobel Laureate Ilya Prigogine (under Prigogine's influence, Milutinovic insisted on separation of temporal and spatial data, for minimal entropy of computing and the best optimization potentials at compile time), (c) by Nobel Laureate Daniel Kahneman (under Kahneman's influence, Milutinovic insisted on approximate computing in the parts of the algorithm where high precision was not needed, in order to save resources for re-investments in the parts of the algorithm that needed the highest precision, which could be of crucial importance for simulations in physics and chemistry), and (d) by Nobel Laureate Tim Hunt (under Hunt's influence, Milutinovic insisted on introduction of compile-time abilities that trade between latency and precision, using transdisciplinary analogies with processes related to birth, reproduction, and death in the cell cycle).

Veljko Milutinovic's Specific Areas of Expertise:

1970s: Architecture/Design of DSP/HLL processors.
1980s: Architecture/Design of GaAs microprocessors/multiprocessors.
1990s: Cache Architecture/Design (split and injection caches).
2000s: Fundamental DataFlow Architecture/Design.
2010s: Advanced DataFlow Architecture/Design.
2020s: Computing Support for Emerging Applications.

His main contributions were to GaAs microprocessor architecture/design and DataFlow supercomputer architecture/design.

@70s[1,2] he contributed to theory/practice of DSP/HLL processors, working for the defense research of exYugoslavia.

@80s[3,4] he created architecture/design of DARPA's first GaAs microprocessor; 200MHz about a decade before commercial microprocessors.

@90s[5,6] with chief architects of SUN/SPARC (Mark Tremblay) and INTEL/x86 (Gad Shaeffer), he introduced the concepts of injection and split caches.

@2000s[7,8] he influenced the creation of one specific DataFlow paradigm. His article on FPGA FloatingPoint Matrix Multiplication won the bi-annual 2014 Premium Award of IET Computers and Digital Techniques.

@2010s[9,10] he influenced DataFlow paradigm advancements. Currently active on DataFlow (re)implementations of Quark and QuasiCrystals processes.

@2020s[11,12] he has two seminal articles accepted for Springer Journal of Big Data, as well as two textbooks on the two courses that he teaches regularly (IGI Global for DataFlowAcceleration and Cambridge Press for SoftwareEngineeringMgmt).

Veljko Milutinovic's Principal Publications, Works, or Contributions to Civic Life:

1970-1980: HLL/DFT Processors

[1] V. Milutinovic, "A Simulation Study of the Vertical-Migration Microprocessor Architecture," IEEE Transactions on Software Engineering, Vol. 13(12), pp. 1265-1277, 1987.

[2] V. Milutinovic, "A Comparison of Suboptimal Detection Algorithms Applied to the Additive Mix of Orthogonal Sinusoidal Signals," IEEE Transactions on Communications, Vol. 36(5), pp. 538-543, 1988.

1980-1990: GaAs Microprocessors/Multiprocessors

[3] J.A.B. Fortes, V. Milutinovic, R.J. Dick, W.A. Helbig, W.D. Moyers, "A High-Level Systolic Architecture for GaAs," Proceedings of the 9th IEEE/ACM Hawaii International Conference on System Sciences (HICSS), Honolulu, Hawaii, January 1986, pp. 253-258 (joint work with: Dick, Helbig, and Moyers of RCA AeroSpace and Defense).

[4] W. Helbig, V. Milutinovic, "A DCFL E/D-MESFET GaAs Experimental RISC Machine," IEEE Transactions on Computers, Vol. 38(2), pp. 263-274, 1989 (joint work with: Helbig of RCA AeroSpace and Defense).

1990-2000: Split and Injection Caches in SMP/DSM:

[5] V. Milutinovic, B. Markovic, M. Tomasevic, M. Tremblay, "The Split Temporal/Spatial Cache: Initial Performance Analysis," Proceedings of the IEEE SCIZZL-5, Santa Clara, California, USA, March 1996 (joint work with: Tremblay of SUN Microsystems, California).

[6] V. Milutinovic, A. Milenkovic, G. Sheaffer, "The Cache Injection CoFetch Architecture: Initial Performance Evaluation," Fifth International Symposium on Modeling, Analysis, and Simulation of Computer and Telecommunication Systems, IEEE MASCOTS, Haifa, Israel, January 12-15, 1997 (joint work with: Shaeffer of Intel, Israel).

2000-2010: Basic DataFlow Paradigm

[7] Z. Jovanovic, V. Milutinovic, "FPGA Accelerator for Floating-Point Matrix Multiplication," IET Computers & Digital Techniques, Vol. 6(4), pp. 249-256, 2012 (supported by Maxeler, California).

[8] M. Flynn, O. Mencer, V. Milutinovic, G. Rakocevic, P. Stenström, R. Trobec, M. Valero, "Moving from Petaflops to Petadata," Communications of the ACM, Vol. 56 (5), pp. 39-42, 2013 (supported by Maxeler, California).

2010-2020: Advanced DataFlow Paradigm

[9] R. Trobec, R. Vasiljević, M. Tomašević, V. Milutinović, R. Beivide, M. Valero, "Interconnection Networks in Petascale Computer Systems: A Survey," ACM Computing Surveys, Vol.49 (3), September 2016, pp. 1-24, 2016 (supported by Maxeler, California).

[10] M. Kotlar, V. Milutinovic, "The Ultimate DataFlow for Tensor Calculus," Invited Talk at the ExaComm Workshop of the International Supercomputing Conference, Frankfurt, Germany, June 28, 2018, published by Springer Nature, 2019 (supported by Maxeler, California).

2020-2030: Advanced DataFlow Applications

[11] ... V. Milutinović, et al, "On Education for DataFlow Applications," Springer Journal of Big Data, 2023 (the leading journal at Clarivate WoS for Data Science – the highest JIF in 2023).

[10] ... V. Milutinovic, et al, "On DataFlow Research for Mathematics, GeoPhysics, and Physics," Springer Journal of Big Data, 2023 (the leading journal at Clarivate WoS for Data Science – the highest JIF in 2023).

Др Вељко Милутиновић, редовни професор у пензији

Електротехнички факултет Универзитета у Београду

БИБЛИОГРАФИЈА

Објављене књиге, поглавља у монографијама и едитоване монографије

Оригиналне књиге/монографије, поглавља у монографијама:

1. V. Milutinovic, F. Djordjevic, M. Kotlar, J. Salom, "Introduction to Management of Complex Software Projects," Cambridge Press, UK, 2024.
2. V. Milutinovic, M. Kotlar (Eds.), "Handbook on Super Computing," IGI Global, USA, FEB 2021.
3. V. Milutinovic, N. Mitic, A. Kartelj, M. Kotlar (Eds) "Dataming for Dataflow," IGI Glob, USA, NOV 2021.
4. V. Milutinovic, M. Kotlar (eds.), "Exploring the DataFlow Supercomputing Paradigm: Example Algorithms for Selected Applications," Springer, 2019.
5. V. Milutinovic, J. Salom, D. Veljovic, N. Korolija, D. Markovic, L. Petrovic, "DataFlow Supercomputing Essentials - Research, Development and Education," Computer Communications and Networks, Springer 2017, ISBN: 978-3-319-66127-8, 150 p., 2017.
6. V. Milutinovic, M. Kotlar, M. Stojanovic, I. Dundic, N. Trifunovic, Z. Babovic, "DataFlow Supercomputing Essentials - Algorithms, Applications and Implementations," Computer Communications and Networks, Springer 2017, ISBN: 978-3-319-66125-4, 150 p., 2017.
7. V. Milutinovic, J. Salom, "Mind Genomics - Guide to Data-Driven Marketing Strategy," Springer, ISBN 978-3-319-39733-7, 125 p., 2016.
8. V. Milutinovic, J. Salom, N. Trifunovic, R. Giorgi, "Guide to DataFlow SuperComputing," Springer, ISBN: 978-3-319-16228-7, 129 p., 2015.
9. G. Jakus, V. Milutinovic, S. Omerovic, S. Tomazic, "Concepts, Ontologies, and Knowledge Representation," Springer Briefs in Computer Science, Springer, ISBN: 978-1-4614-7821-8, pp. i-vi, 1-67, 2013.
10. J. Sodnik, M. Stular, V. Milutinovic, S. Tomazic, "Mobile Communications: 4G," in *Encyclopedia of Wireless and Mobile Communications*, Volume I, CRC Press, 2008.
11. V. Milutinovic, "Infrastructure for E-Business on the Internet," Copyright by Kluwer, ISBN: 978-1-4615-1451-0, 437 p., 2001.
12. V. Milutinovic, "Microprocessor and Multimicroprocessor Systems," Copyright by Wiley, ISBN: 978-0-4713-5728-5, 291 p., 2000.
13. V. Milutinovic, V., "Surviving the Design of a 200 MHz RISC Microprocessor: Lessons Learned," IEEE Computer Society Press, Los Alamitos, California, USA, ISBN: 978-0-8186-7343-6, 200 p., 1997.
14. V. Milutinovic, V., "Projektovanje i arhitektura RISC procesora za VLSI", *Nauka*, Beograd, Srbija, Jugoslavija, 168 p., 1994.
15. V. Milutinovic, D. Bozanic, D. Polomeic, M. Aleksic, "Uvod u projektovanje računarskih VLSI sistema", *Nauka*, Beograd, Srbija, Jugoslavija, 1994., 162 p., 1994.
16. V. Milutinovic, S. Nedic, M. Ostojic, R. Paunovic, "Uvod u mikroprocesore", *Institute Michael Pupin*, Beograd, Srbija, Jugoslavija, 100 p., 1981.

17. V. Milutinovic, "Projektovanje telekomunikacionih uređaja pomoću mikroprocesora", *Institute Michael Pupin*, Beograd, Srbija, Jugoslavija, 258 p., 1978.

Монографије у којима је био едитор:

18. A. Hurson, M. Milutinovic, (eds.), "Advances in Computers, Volume 106," Elsevier, 213p, ISBN: 978-0-12-812230-3, 2017.
19. A. Hurson, V. Milutinovic, (eds.), "Creativity in Computing and DataFlow SuperComputing," Elsevier, 229p, ISBN: 978-0-12-811955-6, 2017.
20. V. Milutinovic, B. Furht, Z. Obradovic, N. Korolija (eds.) "Advances in High Performance Computing and Related Issues," *Mathematical Problems in Engineering*, Hindawi, 2016.
21. A. Hurson, V., Milutinovic, V., "Dataflow Processing, Part 1," Elsevier, 259 p., ISBN: 978-0-12-802134-7, 2015.
22. G. Rakocevic, T. Dukic, N. Filipovic, V. Milutinovic (eds.), "Computational Medicine in DataMining and Modelling," Springer, 376 p., ISBN: 978-1-4614-8785-2, 2013.
23. M. Despotovic-Zrakic, V. Milutinovic, A. Belic (eds.), "High Performance and Cloud Computing in Scientific Research and Education," IGI Global, Hershey, PA, USA, ISBN: 978-1-46-665784-7, 476 p., 2014.
24. L. Gavrilovska, S. Krco, V. Milutinovic, I. Stojmenovic, R. Trobec (eds.), "Application and Multidisciplinary Aspects of Wireless Sensor Networks," *Concepts, Integration, and Case Studies, Computer Communications and Networks*, 1st Edition., 282 p., ISBN: 978-1-84996-509-5, 2011.
25. V. Milutinovic, F. Patricelli (eds.), "E-Business and E-Challenges," Copyright by IOS Press (Italy, USA, Japan, Holland), 2002. Foreword: Jerome Friedman (MIT), Nobel Laureate, 1000 pages, 2002.
26. W.Chin, F. Patricelli, V. Milutinovic (editors), "Electronic Business and Education: Recent Advances in the Internet Infrastructure," Kluwer, Norwell, MA 02061, 2001. Foreword: B. Richardson (Cornell), Nobel Laureate, 450 pages.
27. P. Antognetti, V. Milutinovic (editors, four volume series), "Neural Networks," Prentice Hall, Englewood Cliffs, New Jersey, 1992. Foreword: L. Cooper (Brown), Nobel Laureate, 1207 pages.
28. V. Milutinovic (editor), "Principles of Microprogramming," Prentice-Hall, Englewood Cliffs, New Jersey, 1992. Foreword: M. Wilkes (Cambridge), Turing laureate, 297 pages.
29. V. Milutinovic (editor), "Microprocessor Design for GaAs Technology," Prentice-Hall, Englewood Cliffs, New Jersey, 1990. Foreword: B. Naused (DARPA), 330 pages.
30. V.Milutinovic (editor), "High-Level Language Computer Architecture," Freeman Computer Science Press, Rockville, Maryland, 1989. Foreword: M.Flynn (Stanford), Turing laureate, 474 p.
31. V. Milutinovic (editor), "Computer Architecture: Concepts and Systems," North-Holland, New York, 1988. Foreword: K.Wilson (Cornell), Nobel Laureate, 566 pages.

Едитовани одабрани репринти:

32. J. Protic, M. Tomasevic, V. Milutinovic, V. (eds.), "Tutorial on Distributed Shared Memory," IEEE Press, 1998, 330 pages.
33. I. Tartalja, V. Milutinovic (eds.), "Tutorial on Cache Consistency in Multiprocessor Systems: Software Methods," IEEE Press, 1997, 390 pages.
34. M. Tomasevic, V. Milutinovic (editors), "Tutorial on Cache Consistency in Multiprocessor Systems: Hardware Methods," IEEE Press, 1993, 435 pages.
35. L. Hoevel, V. Milutinovic (eds.), "Proceedings of the ACM Hawaii International Conferences on System Sciences: Computer Architecture," ACM, 1991.
36. B. Shriver, V. Milutinovic (eds.) "Proceedings of the ACM Hawaii International Conferences on System Sciences: Computer Architecture," ACM, 1990.

37. B. Shriver, V. Milutinovic (eds.) "Proceedings of the ACM Hawaii International Conferences on System Sciences: Computer Architecture," ACM, 1989.
38. V. Milutinovic (editor) "Tutorial on Microprogramming and Firmware Engineering," IEEE Press, 1989, 410 pages.
39. V. Milutinovic, D. Fura (editors), "Tutorial on Computer Design of GaAs Technology," IEEE Press, 1988, 354 pages.
40. D. Gajski, V. Milutinovic, H.J. Siegel, B. Furht (editors), "Tutorial on Computer Architecture," IEEE Press, 1987, 593 pages.
An IEEE Computer Society Press Best-Seller of All Times.
41. V. Milutinovic, (editor), "Tutorial on Advanced Microprocessors and High-Level Language Computer Architecture," IEEE Press, 1986, 597 pages.
An IEEE Press Best-Seller.

Едитовани зборници радова са међународних скупова:

42. V. Milutinovic, (editor), Proceedings of VIPSI-2006 Amalfi, Italy, March, 23-26, 2006.
43. V. Milutinovic, (editor), Proceedings of VIPSI-2005 Carcasone, France, April 24-27, 2005.
44. V. Milutinovic, (editor), Proceedings of VIPSI-2004 Studenica, Serbia, June, 10-13, 2004.
45. V. Milutinovic, (editor), Proceedings of IPSI-2003 Sveti Stefan, Montenegro, October, 1-4, 2003.

Радови објављени у научним часописима међународног значаја M20

Радови у врхунским међународним часописима M21a

1. ... V. Milutinovic, et al, "Teaching Computing for Complex Problems in Civil Engineering and Geo Sciences Using Big Data and Machine Learning," Springer Journal of Big Data, 2023.
2. ... V. Milutinovic, et al, "Research in Computing-Intensive Simulations for Nature-Oriented Civil-Engineering and Related Scientific Fields, Using Machine Learning and Big Data," Springer Journal of Big Data, 2023.
3. A. Kos, V. Milutinovic, A. Umek, "Challenges in wireless communication for connected sensors and wearable devices used in sport biofeedback applications," Future Generation Computer Systems, vol. 92, pp. 582-592, 2019, IF: 6,125, (M21a).
4. R. Trobec, R. Vasiljević, M. Tomašević, V. Milutinović, R. Bevide, and M. Valero, "Interconnection Networks in Petascale Computer Systems: A Survey," ACM Computing Surveys, Vol.49 (3), (September 2016), 24 pages, 2016, IF: 6,748 (M21a).
5. M. Flynn, O. Mencer, V. Milutinovic, G. Rakocevic, P. Stenström, R. Trobec, M. Valero, "Moving from Petaflops to Petadata," Communications of the ACM, Vol. 56 (5), pp. 39-42, 2013, IF:2,863 (M21a).
6. I. Ekmecic, I. Tartalja, V. Milutinovic, "A survey of heterogeneous computing: concepts and systems," Proceedings of the IEEE, Vol. 84 (8), pp. 1127-1144, 1996, IF: 2,699/1997 (M21a).
7. B. Perunicic, S. Lakhani, V. Milutinovic, "Stochastic Modeling and Analysis of Propagation Delays in GaAs Adders," IEEE Transactions on Computers, Vol. 40(1), pp. 31-45, 1991, IF: 1,208/1992 (M21a).
8. V. Milutinovic, D. Fura, W. Helbig, "Pipeline Design Tradeoffs in a 32-bit Gallium Arsenide Microprocessor," IEEE Transactions on Computers, Vol. 40(11), pp. 1214-1224, 1991, IF: 1,208/1992 (M21a).

Радови у врхунским међународним часописима M21

9. V. Jelisavcic, I. Stojkovic, V. Milutinovic, Z. Obradovic, "Fast learning of scale-free networks based on Cholesky factorization," *International Journal of Intelligent Systems*, Vol. 33(6), pp. 1322-1339, 2018, IF: 3,363 (M21).
10. Z. Babovic, J. Protic, and V. Milutinovic, "Web Performance Evaluation for Internet of Things Applications," *IEEE Access*, DOI: 10.1109/ACCESS.2016.2615181, Vol. 4, pp. 6974 – 6992, 2016, IF: 3,244 (M21).
11. S. Djordjevic, S. Stancin, A. Meglic, V. Milutinovic, S. Tomasic, "MC Sensor-A Novel Method for Measurement of Muscle Tension," *Sensors*, DOI: 10.3390/s111009411, Vol. 11 (10), pp. 9411-9425, 2011, IF: 1,739 (M21).
12. V. Milutinovic, "Our Profession Needs a Reminder," *IEEE Computer*, Vol. 39(5), pp. 102-104, 2006, IF: 1,289 (M21).
13. N. Milanovic, M. Malek, A. Davidson, V. Milutinovic, "Routing and Security in Mobile Ad Hoc Networks," *IEEE Computer*, Vol. 37(2), pp. 61-65, 2004, IF: 1,432 (M21).
14. V. Milutinovic, N. Skundric, "Will Distance Learning Create a Global University," *IEEE Computer*, Vol. 36(3), pp. 98-100, 2003, IF: 1,552 (M21).
15. E. Jovanov, V. Milutinovic, A. Hurson, "Acceleration of Nonnumeric Operations Using Hardware Support for the Ordered Table Hashing Algorithms," *IEEE Transactions on Computers*, Vol. 51(9), pp. 1026-1040, 2002, IF: 1,484 (M21).
16. A. Ngom, I. Stojmenovic, V. Milutinovic, "STRIP - a strip-based neural-network growth algorithm for learning multiple-valued functions," *IEEE Transactions on Neural Networks*, Vol. 12(2), pp. 212-227, 2001, IF: 1,479 (M21).
17. V. Milutinovic, P. Knezevic, B. Radunovic, S. Casselman, J. Schewel, "Obelix Searches Internet Using Customer Data," *IEEE Computer*, Vol. 33(7), pp. 104-107, 2000, IF: 1,043 (M21).
18. V. Milutinovic, D. Cvetkovic, J. Mirkovic, "Genetic Search Based on Multiple Mutations," *IEEE Computer*, Vol. 33(11), pp. 118-119, 2000, IF: 1,043 (M21).
19. V. Milutinovic, "Caching in distributed systems," *IEEE Concurrency*, Vol. 8(3), pp. 14-15, 2000, IF: 1,018 (M21).
20. M. Jovanovic, V. Milutinovic, "An overview of reflective memory systems," *IEEE Concurrency*, Vol. 7(2), pp. 56-64, 1999, IF: 0,784 (M21).
21. V. Milutinovic, M. Valero, "Enhancing and Exploiting the Locality," *IEEE Transactions on Computers*, Vol. 48(2), pp. 97-99, 1999, IF: 1,057 (M21).
22. I. Tartalja, V. Milutinovic, "Classifying Software-Based Cache Coherence Solutions," *IEEE Software*, Vol. 14(3), pp. 90-101, 1997, IF: 0,768 (M21).
23. D. Milutinovic, V. Milutinovic, "New solutions for new technologies," *IEEE Computer*, Vol. 29(4), 1996, IF: 0,608/1997 (M21).
24. A. Grujic, M. Tomasevic, V. Milutinovic, "A simulation study of hardware-oriented DSM approaches," *IEEE Parallel & Distributed Technology Systems*, Vol. 4(1), pp. 74-83, 1996, IF: 0,574/1997 (M21).
25. J. Protic, M. Tomasevic, V. Milutinovic, "Distributed shared memory: concepts and systems," *IEEE Parallel & Distributed Technology Systems*, Vol. 4(2), pp. 63-71, 1996, IF: 0,574/1997 (M21).
26. V. Milutinovic, Z. Petkovic, "Ten Lessons Learned from a RISC Design," *IEEE Computer*, Vol. 28(3), pp. 120, 1995, IF: 0,608/1997 (M21).
27. I. Ekmeccic, I. Tartalja, V. Milutinovic, "EM³: A Taxonomy of Heterogeneous Computing Systems," *IEEE Computer*, Vol. 28(12), pp. 68-70, 1995, IF: 0,608/1997 (M21).

28. M. Tomasevic, V. Milutinovic, "Hardware approaches to cache coherence in shared-memory multiprocessors, Part 1," IEEE Micro, Vol. 14(5), pp. 52-59, 1994, IF: 0,992/1997 (M21).
29. M. Tomasevic, V. Milutinovic, "Hardware approaches to cache coherence in shared-memory multiprocessors 2.," IEEE Micro, Vol. 14(6), pp. 61-66, 1994, IF: 0,992/1997 (M21).
30. W. Helbig, V. Milutinovic, "A DCFL E/D-MESFET GaAs Experimental RISC Machine," IEEE Transactions on Computers, Vol. 38(2), pp. 263-274, 1989, IF: 1,388/1988 (M21).
31. V. Milutinovic, M. Bettinger, W. Helbig, "Multiplier/Shifter Design Tradeoffs in a 32-bit Microprocessor," IEEE Transactions on Computers, Vol. 38(6), pp. 874-881, 1989, IF: 1,388/1988 (M21).
32. V. Milutinovic, "A comparison of suboptimal detection algorithms applied to the additive mix of orthogonal sinusoidal signals," IEEE Transactions on Communications, Vol. 36(5), pp. 538-543, 1988, IF: 0,802 (M21).
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77. V. Milutinovic, "HF Radio Modem: Software Issues," International Michael Pupin Conference on HF Data Modem Desing, 1981.
78. V. Milutinovic, "HF Radio Modem: Hardware Aspects," Euromicro Conference, 1980.
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Радови у часописима националног значаја М50

Радови у научним часописима М53

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2. Milutinovic, V., "A Structured Approach to Research for PhD Students in Computer Science and Engineering: How to Create Ideas, Conduct Research, and Write Papers," The IPSI BgD Transactions on Internet Research, vol. 11(2), pp. 46-54, 2015.
3. A. Mihajlović, V. Jelisavčić, B. Marinković, Z. Ognjanović, V. Milutinović, "An Overview of and Innovative Perspectives for the Serbia-Forum Cultural Heritage Digitization Project," Review of the National Center for Digitization, Faculty of Mathematics, Vol. 25, pp. 17-21, 2014.
4. V. Milutinović, J. Salom, V. Jelisavčić, V. Filipović, A. Mihajlović, Z. Ognjanović, Z. Marković, A. Kos, S. Tomažič, H. Maurer, "Heritage Portals and Heritage Mining: Synergizing Data and Image Mining Under Uncertainty Constraints," The IPSI BgD Transactions on Internet Research, Vol. 10 (2), pp. 53-56, 2014.
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7. S. Tomazic, V. Milutinovic, "Hot Topics in Computer Science and Engineering," The IPSI BgD Transactions on Internet Research, vol. 6(1), pp. 31-35, 2010.
8. Z. Babovic, D. Jovic, V. Milutinovic, "Survey of eGovernment Services in Serbia," Informatica (Slovenia), vol. 31(4), pp. 379-396, 2007.
9. V. Milutinovic, S. Tomazic, "How to Ruin the Carrier of a Ph.D. Student: Precise Guidelines," The IPSI BgD Transactions on Advanced Research, vol. 3(2), pp. 1-2, 2007.
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Техничка Решења М80

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2. Jelisavčić V., Korolija N., Babović Z., prof. Bojović M., prof. Milutinović V. „Sistem za obučavanje neuralnih mreža na velikim podacima zasnovan na Apache Spark platformi”, 2015 (**M85 – Softver**).
3. Mihajlovic A., Marinković B., Milutinović V., Jelisavčić V., Ognjanović Z., Knežević M., "Serbia Forum," 2015 (**M84 – Bitno poboljšani softver**).
4. V. Milutinovic, "Digital Data Modem Design," Purdue University Technical Report, 1985.

arXiv

1. V. Milutinovic, M. Djordjevic, E. Azer, K. Yoshimoto, K. (IU), G. Klimeck (Purdue), A. Tsuda (Harvard), M. Kosanic, M. Ilic (MIT), Z. Babovic, N. Filipovic (UKG), M. Bojovic, N. Korolija, M. Kotlar, B. Miladinovic, S. Stankovic, N. Trifunovic (ETF), M. Valero (UPC), M. Desanto (USF), J. Skorucak (ETH), E. Neuhold (UNIWIIE), I. Ratkovic (ET, San Francisco, California, USA), L. Dipietro (HI, Boston, Massachusetts, USA), J. Salom (Israel), "**The Ultimate DataFlow for Ultimate SuperComputer-on-a-Chip,**" arXiv, 31.1.2021.
2. M. Kosanic (ETF), V. Milutinovic (IU), M. Ilic (MIT), "**A Survey of Mathematical Aspects of Machine Learning in Geophysics: The Cases of Weather Forecast, Wind Energy, Oil and Gas Exploration,**" arXiv, 31.1.2021.

Међународни и национални пројекти

Пројекти које је радио кроз Универзитет у Београду:

1. МПНТР ИИИ44006 - Развој нових информационо-комуникационих технологија, коришћењем напредних математичких метода, са применама у медицини, телекомуникацијама, енергетици, заштити националне баштине и образовању, Руководилац потпројекта, 2011-2016.
2. Иновациони пројекат МПНТР #N5QQ5C: Примена метода за проналажење знања над великом количином података, 2014-2015.
3. Иновациони пројекат МПНТР #451-03-00605/2012-16/198: Клауд сервиси за апликације са захтевима за високим перформансама, 2011-2012.
4. ЕУ FP7 пројекат #288076 BALCON: Boosting EU-Western Balkan Countries research collaboration in the Monitoring and Control area, 2010-2012.
5. ЕУ FP7 пројекат #205494 ProSense: Promote, Mobilize, Reinforce and Integrate Wireless Sensor Networking Research and Researchers: Towards Pervasive Networking of West Balkan Countries and the EU, 2007-2010.
6. Иновациони пројекат МПНТР #391-00-00027/2009-02/142: Софтвер за аквизицију, мониторинг и обраду података са сензорских мрежа у систему даљинског грејања, 2009-2010.
7. ЕУ FP7 пројекат #224297 ARTreat: Multi-level patient-specific artery and atherogenesis model for outcome prediction, decision support treatment, and virtual hand-on training, 2009-2011.
8. ЕУ FP7 пројекат HiPEAC: European Network of Excellence on High Performance and Embedded Architecture and Compilation, 2008-2012.
9. ЕУ FP6 пројекат #045472 We-Go: Enhancing Western Balkan eGovernment expertise, 2007-2008.
10. Иновациони пројекат МПНТР #451-01-02960/2006-70: Интероперабилни оквир за е-говернмент сервисе, 2007-2008.
11. Иновациони пројекат МПНТР #451-01-0065/2008-01/128: Мрежа знања е-Говернмент сервиса, 2008-2009.

Пројекти које је радио 80их година кроз Purdue University:

12. DARPA GaAs 200MHz RISC Microprocessor
13. DARPA GaAs Systolic Array for Gram-Schmidt Orthogonalization
14. NCR Basic VM Architecture for High-Level Languages
15. NCR Improved VM Architecture for High-Level Languages

Пројекти које је радио 70их у Институту Михајло Пупин

16. КТ радио модем (17 чврсто спрегнутих микропроцесора који раде ДФТ и arctg)
17. ССИТТ модем за телефонске канале на брзини 4800 бита/сек.

Индустријско истраживање и иновације:

18. Cache Injection, Intel, USA.
19. High-performance computer architecture, TechnologyConnect.
20. High-performance computer architecture, BioPop.
21. КТ modem, IBM.
22. High-performance computer architecture, AT&T.
23. High-performance computer architecture, RCA.
24. High-performance computer architecture, Honeywell.
25. High-performance computer architecture, Fairchild.
26. High-performance computer architecture, HP.
27. High-performance computer architecture, Encore.
28. Split Cache за Sun Microsystems.
29. Иновације за фирму NCR током 90их година. Подршка за 8 тадашњих постдипломаца ЕТФ-а.
30. Имплементација микропроцесора Intel i860 у .isp за силицијумску компилацију у сложеним микропроцесорским системима (90их), описана до детаља у раду објављеном у IEEE Computer.
31. Educator P2P, Panthesis, for Boeing, USA, 2004.
32. Multimedia, IPSI-Fraunhofer Institute, Germany, Darmstadt, 2003-2004.
33. Иновације у домену Storage Technologies, за фирму StorageTek (касније Sun Microsystems, па Oracle Inc.) (после 2000).
34. Online betting, Finsoft, UK, London (касније GTECH, USA) (2005-2015).
35. Software Cache Management, Dow Jones, USA, 2006.
36. Banking applications, Komercijalna Banka, Srbija. (после 2010).
37. Banking applications, Banca Intesa, Srbija (после 2010).
38. Banking applications, Eurobank a.d., Srbija (после 2010).
39. ERP applications, Delta Holding, Srbija (после 2010).
40. Иновације у домену DataFlow, за фирму Maxeler Technologies (после 2010), описане у раду објављеном у часопису Communications of the ACM и више књига издатих од Springer и Elsevier издавача.

Предавања по позиву

Одабрана предавања по позиву изван Европе (100) на актуелне теме из архитектуре рачунара (GaAs, RISC, DSM, DataFlow):

USA:

1. U of California, San Diego, 1983
2. U of California, Irving, 1985
3. UCLA, 1985
4. USC, 1985
5. U of California, Berkeley, 1991
6. Stanford, 1991
7. U of California, Santa Barbara, 1995
8. U of California, Santa Clara, 1995
9. Lawrence Livermore National Labs, 1997
10. Jet Propulsion National Labs, 1997
11. Fairchild, California, 1985
12. Intel, Oregon, 1986

13. Honeywell, Minnesota, 1986
14. CDC, Minnesota, 1986

15. ElectroSpace, Texas, 1987
16. AeroSpace, Texas, 1987

17. NCR, Dayton, Ohio, 1989
18. Ohio State U, 2016

19. Indiana U, Bloomington, 2016, 2017, 2018, 2019, 2020, 2021, 2022, 2023.
20. Purdue U, West Lafayette, Indiana, 1983-2001, 2012-2023
21. IUPUI, Indianapolis, 2012
22. U of Indianapolis, 2012

23. UIUC, Urbana-Champaign, 2017, 2023
24. U of Chicago, 1985

25. U of Wisconsin, 2016
26. U of Michigan, 2016

27. U of Colorado in Boulder, 1987
28. U of Missouri in St. Louis, 1982

29. U of Tennessee in Louisville, 2016
30. U of Georgia in Atlanta, 2016

31. FAU, Boca Raton, Florida, 2014
32. FIU, Miami, Florida, 2014
33. U of Miami, Florida, 2014
34. U of Central Florida, Orlando, 2014

35. U of Alabama, Huntsville, 2013
36. U of Alabama, Birmingham, 2013

37. Princeton, 2017
38. NJIT, New Jersey, 2017
39. RCA, New Jersey, 1983, 1984, 1985, 1986, 1987
40. ATT, New Jersey, 1989

41. World Bank, Washington DC, 2016
42. U of Mariland, 1989

43. CMU, Pittsburgh, 2016, 2019
44. Temple, Philadelphia, 2016

45. Yale, 2005
46. Brown, 2005

47. NYU, 2003, 2018
48. CUNY Albany, 2005
49. Columbia, 2015
50. Brooklyn Poli, 1985
51. Yahoo, NY, 2015
52. IBM TJWatson, 1989, 2015, 2017
53. CUNY Binghampton, 1988
54. Cornell, 1985
55. Brooklyn Poli, NY, 1985
56. Mahnattanville U, 2015
57. Fordham U, 2015
58. Courant School of Math, 2018

59. MIT, 2009, 2010, 2011, 2012, 2013, 2014, 2015, 2016, 2017, 2018
60. Harvard, 2016, 2017
61. Boston U, 2017
62. NEU, 2017
63. Dartmouth U, 1988
64. U of Massachussets in Amherst, 2017

65. U of Hawaii at Manoa, 1990
66. HICSS, 1990, 1991, 1992, 1993, 1994, 1995, 1996, 1997, 1998, 1999, ...

CANADA:

67. U of Toronto, 1985
68. U of Montreal, 2002
69. U of St Marry in New Hampshire, 1995
70. U of New Hampshire in Halifax, 1995

MEXICO:

71. Durango U, 2000, 2001, 2002
72. UNAM, Mexico City, 2002
73. U of Cuarnavaca, 2001
74. U of Puebla, 2001

- 75. Tech de Monterey, 2005
- 76. U of Zacatecas, 2003

AUSTRALIA:

- 77. U of Hobart, 1988
- 78. U of Brisbane at Gold Coast, 1992
- 79. Sydney U, 1988
- 80. Sydney U of Technology, 1988

JAPAN:

- 81. Tokyo U, 1985
- 82. Metropolitan Institute of Tokyo, 2010
- 83. Senday U, 2010
- 84. OKI Data, Tokyo, 1985

KOREA:

- 85. ISCA, 1997
- 86. Hyundai, 1997

CHINA:

- 87. Tsinghua, 2017
- 88. Shandong, 2017

SINGAPORE:

- 89. NTU, 1998
- 90. NIS, 1998

TURKEY:

- 91. Bogazici, Istanbul, 2011, 2013, 2015
- 92. Koc, Istanbul, 2015
- 93. Gazi U, Ankara, 2016
- 94. METU, Ankara, 2016

IZRAEL:

- 95. Technion, 1988
- 96. BerSheba, 1988
- 97. Rafael, Netania, 1988
- 98. Gabriel, Netania, 1988
- 99. ISCA, Eilat, 1988
- 100. U of Haifa, 1988

EVROPA:

Предавања по позиву у Европи су држана у 33 разне земље (укупно преко 300 предавања):

Србија, Црна Гора, Македонија, Босна и Херцеговина, Хрватска, Словенија, Албанија, Грчка, Бугарска, Румунија, Мађарска, Словачка, Чешка, Пољска, Русија, Естонија, Финска, Шведска, Норвешка, Немачка, Аустрија, Швајцарска, Италија, Француска, Белгија, Луксембург, Холандија, Енглеска, Велс, Ирска, Шпанија, Португалија и Кипар.

Линкови са доказима у вези најбитнијих навода:

Докази за сваки наведени детаљ постоје у документима (доступни на увид)
или на следећим линковима:

Purdue University, CE rang #9 (12 godina)

<https://engineering.purdue.edu/Engr/AboutUs/FactsFigures/Rankings>

Indiana University, IT rang #7 (do sada 6 godina i dalje na neodredjeno vreme)

https://cs.indiana.edu/contact/profile/index.html?Veljko_Milutinovic

Predavao je u Becu i na Matematickom UNIWIIE i na Tehnickom TUWIEN:

<https://ufind.univie.ac.at/en/course.html?lv=052820&semester=2018S>

<https://informatics.tuwien.ac.at/guests/> (GOSTUJUCI PROFESOR IZ SAD)

Sada predaje u Beogradu na MATF-u i ETF-u (ranije FON i FFH):

Za MATF se moze videti raspored casova za tekuci semestar.

Za ETF <http://rti.efg.ac.rs/rti/ms1ker> (GOSTUJUCI PROF IZ SAD)

U ovom SCI radu je okupio sve tadasnje doktore nauka sa ETF-Racunarstvo:

https://scholar.google.com/citations?user=qulqFtIAAAJ&hl=sr&oi=ao#d=gs_md_cita-d&u=%2Fcitations%3Fview_op%3Dview_citation%26hl%3Dsr%26user%3DqulqFtIAAAJ%26cstart%3D20%26pagesize%3D80%26citation_for_view%3DqulqFtIAAAJ%3A0izLItjtcgwC%26tzom%3D-60

Na ovom SCI radu su svi tadasnji doktori nauka sa MATF-Racunarstvo, osim jednog:

https://scholar.google.com/citations?user=qulqFtIAAAJ&hl=sr&oi=ao#d=gs_md_cita-d&u=%2Fcitations%3Fview_op%3Dview_citation%26hl%3Dsr%26user%3DqulqFtIAAAJ%26cstart%3D200%26pagesize%3D100%26citation_for_view%3DqulqFtIAAAJ%3AMlq0yeAD4gqC%26tzom%3D-60

U ovoj knjizi su svi doktori nauka sa katedre na FON-u kojoj pomaze:

<https://www.igi-global.com/book/handbook-research-high-performance-cloud/94528>

U ovoj knjizi su kolege sa свих univerziteta u EU/US sa kojima saradjuje:

<https://www.igi-global.com/book/handbook-research-methodologies-applications-supercomputing/261119>